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High thermoelectric power factor in SnSe₂ thin film grown on Al₂O₃ substrate

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Abstract

Thermoelectric figure of merit (ZT) is highly sensitive to the carrier concentration and maximizes within the narrow region of $10^{19} - 10^{20} \text{ cm}^{-3}$. The SnSe₂ single crystal is predicted to have a high ZT value with carrier concentration in the range of $10^{19} - 10^{20} \text{ cm}^{-3}$. Here, we grew SnSe₂ thin film on Al₂O₃ substrate by Pulsed Laser Deposition (PLD) with post annealing at 400 °C in Argon for 60 min. The annealed thin film shows a high thermoelectric power factor up to $8 \mu\text{Wcm}^{-1}\text{K}^{-2}$ at 220 K with a carrier concentration of $5.2 \times 10^{19} \text{ cm}^{-3}$. A hexagonal crystal structure of the SnSe₂ thin film was confirmed by x-ray diffraction and Raman spectra measurements. The thin film showed an n-type semiconductor behavior. Maximum electrical conductivity and Seebeck coefficient were obtained at 220 K with the values of 210 S.cm^{-1} and $-192 \mu\text{VK}^{-1}$, respectively.

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1. Introduction

Thermoelectric materials have been attracted the interest of researchers as it directly converts heat energy (including the wasted energy of heat) to electrical energy, and vice versa. A thermoelectric material is evaluated by the thermoelectric figure of merit ($ZT = S^2\sigma T/\kappa$) value, where S , σ , κ and T represent the Seebeck coefficient, electrical conductivity, thermal conductivity, and absolute temperature, respectively. [1,2] To increase ZT value, thermoelectric power factor, which is defined as the $PF = S^2\sigma$ should be maximized and thermal conductivity (κ) should be minimized. Thermoelectric power factor can be enhanced through the optimized carrier concentration, energy filtering of charge carriers, or increase the mobility (μ) of the charge carrier while thermal conductivity can be reduced by adding the number of interfaces and phonon scattering centers in nanostructure such as nanowire, nanotube, superlattice, or nanocomposite.[3–6] In recent years, the devices for local cooling and micro-thermoelectric power generation devices, which have been used Peltier and Seebeck effects in thermoelectric thin films are attracting the interest of electronic manufacturers for applications in the mobile and wireless devices. Therefore, many research groups have been trying to enhance the thermoelectric performance of thin films. For examples, A. Hmood *et al.* observed high thermoelectric power factors up to $45.25 \mu\text{W cm}^{-1} \text{K}^{-2}$ in the $\text{Pb}_{0.925}\text{Yb}_{0.075}\text{Se}_{0.2}\text{Te}_{0.8}$ and $31.3 \mu\text{W cm}^{-1} \text{K}^{-2}$ in the $\text{Sn}_{0.9}\text{Yb}_{0.1}\text{Te}$ thin films, and $18.7 \mu\text{W cm}^{-1} \text{K}^{-2}$ in the $\text{AgPb}_8\text{SbTe}_{10}$ sample. [7–9] Phuoc-Huu Le *et al.* observed a high thermoelectric power factor of $24.3 \mu\text{W cm}^{-1} \text{K}^{-2}$ in the Bi_2Te_3 thin film on SiO_2/Si (100) substrate.[10] By doping Pb into Bi_2Te_3 , Yang Zhou et al. obtained power factor up to $25 \mu\text{W cm}^{-1} \text{K}^{-2}$ at 473 K.[11] H. Chang et al. achieved the maximum power factor of $12.4 \mu\text{W cm}^{-1} \text{K}^{-2}$ by annealing Sb_2Te_3 thin film at 220°C for 60 min.[12] By using the p-type $\text{Sn}_{0.9}\text{Yb}_{0.1}\text{Te}:\text{Te}$ and n-type $\text{Sn}_{0.9}\text{Yb}_{0.1}\text{Te}$, A. Hmood *et al.* fabricated the micro-thermoelectric device with the maximum open-circuit voltage output of 823.7 mV and a maximum output power of $0.259 \mu\text{W}$ at a temperature difference of 180 K and the hot-side temperature (T_h) of 528 K.[13]

Among the layer structure semiconductors family, SnSe and SnSe_2 were predicted as the good materials for thermoelectric applications due to their ultra-low thermal conductivity. Experimental results showed that ZT values of the orthorhombic SnSe single crystal can be achieved to 2.6 at 933 K for p-type SnSe and 2.2 at 733 K for n-type with doping of Bi

1 while theoretical calculation predicted that ZT value of SnSe₂ single crystal can be
2 achieved up to 2.95 with the carrier concentration of 10²⁰ cm⁻³. [14–16] SnSe₂ has a
3 hexagonal crystal structure of the type CdI₂ with $P\bar{3}m1$ space group, which is
4 characterized by a van der Waals bonding force between Sn-Se-Sn layers along the c-axis
5 direction. Lattice parameters of hexagonal SnSe₂ are a = b = 3.811 Å and c = 6.141 Å. [17]
6 SnSe₂ has been identified as an n-type semiconductor with an indirect band gap of 0.97
7 eV. [18] Recently, several research groups have been focused on the enhancement of
8 thermoelectric performance of bulk SnSe₂ and achieved some promising results. By doping
9 Cl, Peipei Xu *et al.* obtained maximum PF of 7.0 μW cm⁻¹ K⁻² and ZT value of 0.4 in the
10 6% of Cl substituting in SnSe₂ polycrystalline. [19] Fu Li *et al.* obtained the highest PF of
11 4 μW cm⁻¹ K⁻² and ZT value of 0.4 in the Sn_{0.99}Ag_{0.01}Se₂. [20], Yixuan Wu *et al.* enhanced
12 ZT of SnSe₂ up to 0.6 at 750 K by substituting Br into Se sites of SnSe₂. [21]
13 (SnSe₂)_{0.67}(Bi₂Se₃)_{0.33} compound has PF = 3.1 μW cm⁻¹ K⁻². [22] Additionally, other
14 properties such as electrical transport and optical properties of SnSe₂ were also
15 studied. [23–28] For SnSe₂ thin films, most of the reports focused on characteristic
16 structure, electrical, and optical properties. [29–33] Some groups have reported on the
17 growth condition of SnSe₂ thin films. [34–36] To best of our knowledge, there has been
18 no study about thermoelectric properties of the SnSe₂ thin film. This article introduces a
19 study on thermoelectric properties of the SnSe₂ on Al₂O₃ substrate in which the power
20 factor value can be achieved up to 8 μWcm⁻¹K⁻² at 220 K.

2. Experiment

21 SnSe₂ thin films were grown on an Al₂O₃ substrate by Pulsed Laser Deposition (PLD)
22 method from a SnSe₂ single crystal target. The SnSe₂ target was fabricated from high purity
23 (5N) of Sn and Se powder by temperature gradient method under following process. After
24 loading materials inside, the quartz ampoule was sealed under vacuum (> 10⁻³ Torr).
25 Materials in quartz ampoule was slowly heated to 750 °C and maintained at this
26 temperature for 10 h. Finally, it was slowly cooled down to room temperature. Obtained
27 SnSe₂ single crystal with the dimension of 14 mm was used as a target for PLD growth.
28 Al₂O₃ substrate was chemically cleaned by methanol before being loaded into the growth
29 chamber. A krypton fluoride (KrF) excimer laser (λ = 248 nm, CompexPro 102F) with 20
30 ns pulse width was used for deposition of SnSe₂ thin film. The thin film was deposited by
31

1
2 pulsed of laser repetition rate of 3 Hz in 20 min under a base pressure of 10^{-4} Torr and the
3
4 substrate temperature of 100 °C. Haft of SnSe₂ thin film was annealed in Argon gas at 400
5
6 °C for 60 min. The crystal structure of the thin film was characterized by X-ray diffraction
7
8 [model D/max-RC, Rigaku Co., Tokyo, Japan]. Surface morphology was characterized by
9
10 a field emission scanning electron microscope (FE-SEM). Physical property measurement
11
12 system (PPMS) was used to characterize the electrical resistivity, the Seebeck coefficient,
13
14 and the Hall resistance.

15 16 3. Results and Discussions

17
18 The crystal structure of SnSe₂ bulk (as the target) and thin film were confirmed by XRD
19
20 patterns as shown in Fig. 1 (a). The locations of diffraction peaks of both target and film
21
22 indicated a single phase - hexagonal structure of SnSe₂. Only (00l) diffraction peak group
23
24 indicated that SnSe₂ single crystal was successfully fabricated by the gradient temperature
25
26 method. Two diffraction peak groups of (00l) and (h0l) appeared in the SnSe₂ thin film
27
28 corresponding a polycrystalline nature. By using Bragg's Law, interplanar distance d_{001} (c
29
30 lattice constant) of the SnSe₂ thin film was determined to be 6.173 Å. It is a little larger
31
32 than that of SnSe₂ bulk. An in plane compress strain due to the lattice mismatch between
33
34 Al₂O₃ substrate and SnSe₂ thin film may be the cause of the increase the c lattice constant
35
36 and the shift of XRD peaks. Fig. 1 (b) shows the Raman spectrum from 100 to 300 cm⁻¹ at
37
38 room temperature for the SnSe₂ thin film. Two Raman vibration modes E_g and A_{1g} appear
39
40 at 119.1 cm⁻¹ and 185.7 cm⁻¹ are characteristic of the hexagonal SnSe₂ phase. Surface
41
42 morphologies of cleaved SnSe₂ bulk crystal and thin film were observed by FE-SEM
43
44 images as shown in Fig. 2. A lamellar microstructure with few μm of average thickness
45
46 was observed in the cleaved bulk target while the surface of the SnSe₂ thin film is relatively
47
48 uniform. The thickness of the thin film as shown in the inset of Fig. 2 (b) is 85 nm.

49
50 Hall resistance, which is a function of an external magnetic field at the selected temperature
51
52 and temperature dependent carrier concentration from 50 to 400 K of SnSe₂ thin film, are
53
54 shown in Fig. 3. The negative slopes of a Hall resistance indicated an existence of an n-
55
56 type semiconductor behavior in the SnSe₂ thin film. From the slope of V_H/I against the
57
58 magnetic field, the carrier concentrations of the SnSe₂ thin film was calculated using the
59
60 formula, $\frac{V_H}{I} = \frac{1}{ned}H$, where V_H is the Hall voltage, I is the electrical current, n is the number
of carriers, e is the electrical charge, H is the magnetic field, and d is the thickness of thin

1
2 film. Carrier concentration increases from 4.8×10^{19} to $6.2 \times 10^{19} \text{ cm}^{-3}$ with an increment of
3
4 temperature from 50 to 400 K. As the prediction of theoretical calculations, this region
5
6 carrier concentration is a good sign for thermoelectric properties of SnSe₂. [16,37]
7

8 The temperature-dependent electrical conductivity of the SnSe₂ thin film is shown in Fig.
9
10 4 (a). The SnSe₂ thin film exhibited semiconductor behavior in all temperature range. The
11
12 electrical conductivity increase with increasing temperature from low temperature to 220
13
14 K. But at the temperature higher than 220 K the decrease of electrical conductivity may
15
16 due to the competing factor of carrier concentration (n) and carrier mobility (μ). [7]
17
18 Maximum electrical conductivity is 212.7 S.cm^{-1} at 220 K, comparable to some
19
20 experimental results in the bulk SnSe₂ crystal with the Cl or Br doping [19,21] and similar
21
22 to the theoretical calculation result at the same carrier concentration order of D. Yingchun
23
24 et al. [16] The enhancement of carrier concentrations is the cause of high electrical
25
26 conductivity in the annealed SnSe₂ thin film. A similar trend with electrical conductivity
27
28 was observed in the temperature dependent Seebeck coefficient as shown in Fig. 4 (b). The
29
30 negative Seebeck coefficient confirmed for n-type semiconductor behavior of the sample.
31
32 The value of the Seebeck coefficient sharply increases from low temperature until 220 K
33
34 with a maximum value of $-192 \mu\text{VK}^{-1}$ and slowly reduce with higher temperature. The
35
36 different of Seebeck coefficient between below and above 220 K also attributed for the
37
38 competing of carrier concentration and carrier mobility.

39 Thermoelectric power factor (PF), which is an important parameter of thermoelectric
40
41 materials is determined from electrical conductivity and Seebeck coefficient results as
42
43 shown in Fig. 5. Highest Power factor value of annealing SnSe₂ thin film is $8 \mu\text{Wcm}^{-1}\text{K}^{-2}$
44
45 at 220 K. Comparable to experimental results of pure SnSe₂ bulk crystalline, PF of
46
47 annealing SnSe₂ thin film is greatly improved.

48 **4. Conclusion**

49
50 The high thermoelectric power factor is obtained in a hexagonal structure SnSe₂ thin film
51
52 grown on Al₂O₃ substrate. By annealing effects, we can achieve carrier concentration order
53
54 in region of $10^{19} - 10^{20} \text{ cm}^{-3}$, which is the best value for thermoelectric performance.
55
56 Electrical conductivity increased significantly in thin films compared to that of bulk
57
58 samples. The high thermoelectric power factor of $8 \mu\text{Wcm}^{-1}\text{K}^{-2}$ can be attributed to that
59
60 the high electrical conductivity due to annealing while maintaining a high Seebeck

1
2 coefficient. With a high-power factor value, SnSe₂ thin film is a promising material for
3 thermoelectric micro-devices.
4

5
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Figure captions

Fig. 1. (color online) (a) Room temperature XRD patterns of bulk target SnSe₂ and annealed SnSe₂ thin film on Al₂O₃ substrate. (b) Room temperature Raman spectrum of annealed SnSe₂ thin films.

Fig. 2. (color online) FE-SEM images of (a) cleaved bulk target SnSe₂ and annealed SnSe₂ thin film. Inset of (b) is FE-SEM cross-section image for the determined thickness of thin film.

Fig. 3. (color online) (a) V_{Hall}/I versus magnetic field curves of annealed SnSe₂ thin film at the selected temperature, (b) Temperature-dependent carrier concentration of annealed SnSe₂ thin film.

Fig. 4. (color online) (a) Electrical conductivity as a function of temperature, (b) Temperature-dependent Seebeck coefficient of annealed SnSe₂ thin film.

Fig. 5. (color online) Temperature-dependent thermoelectric power factor of annealed SnSe₂ thin film.

Fig. 1

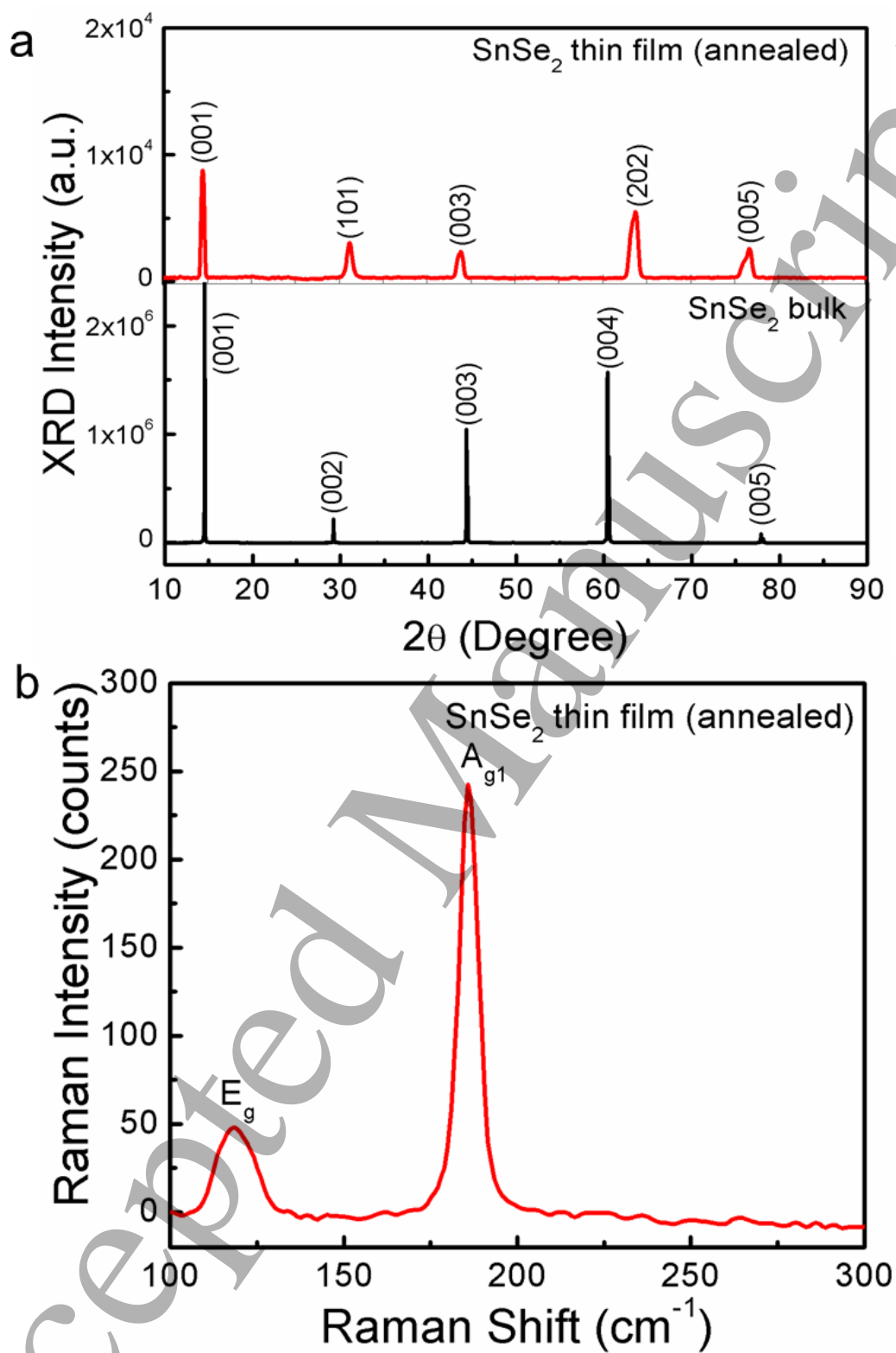


Fig. 2

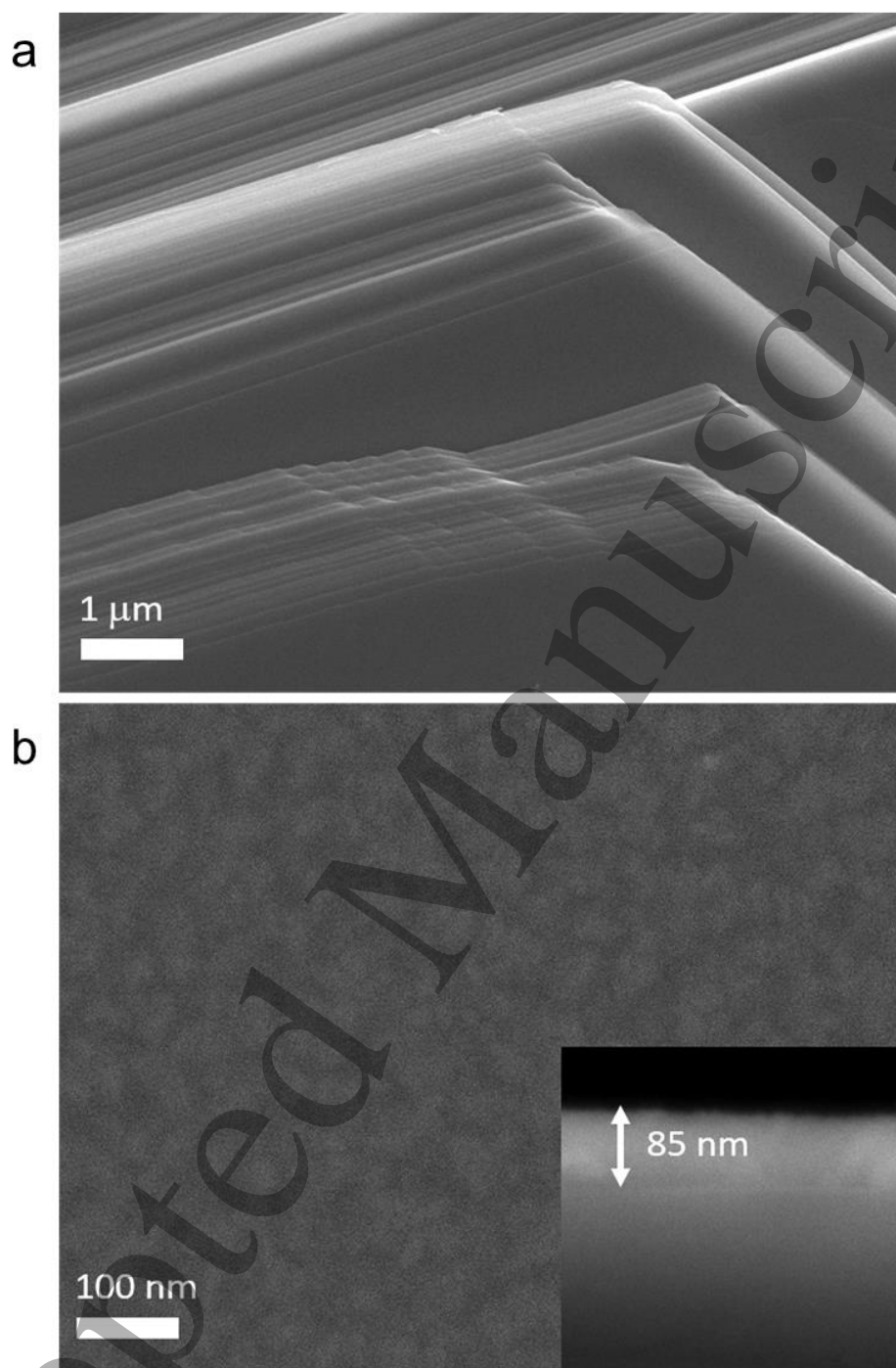


Fig. 3

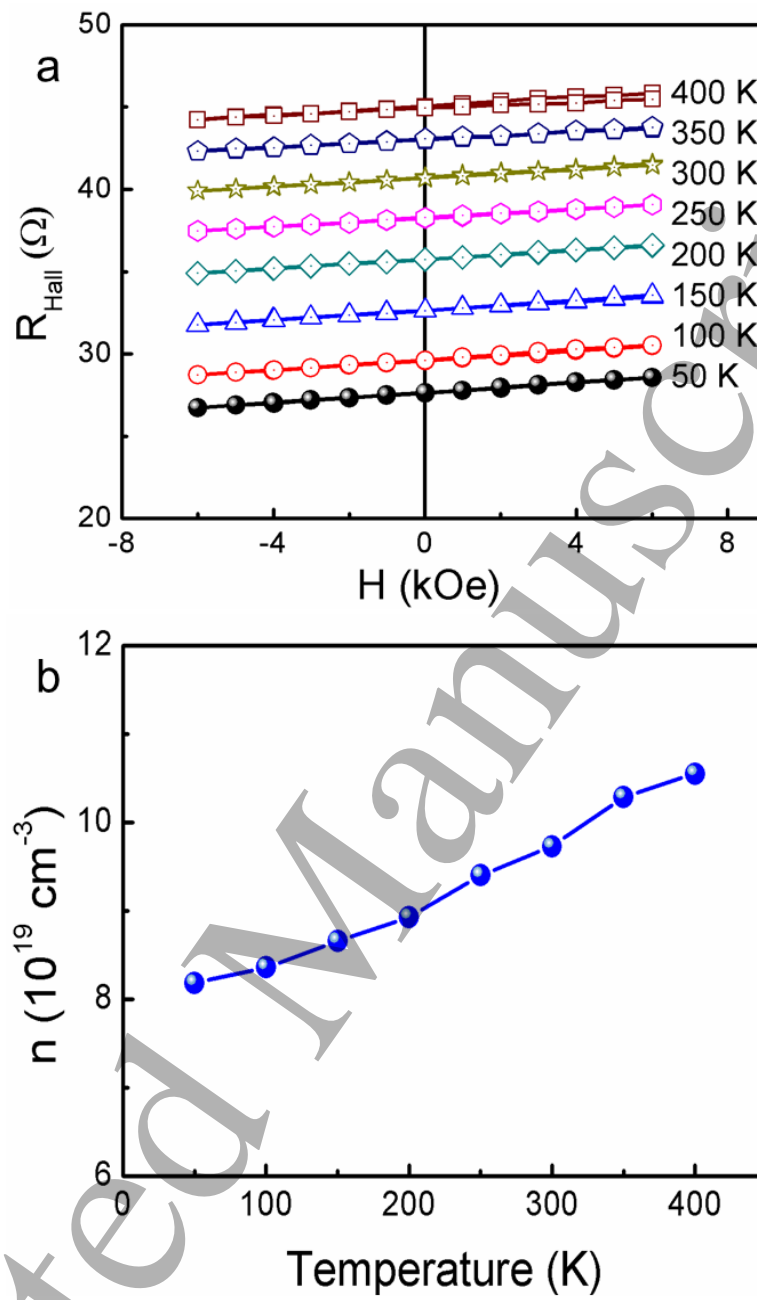


Fig. 4

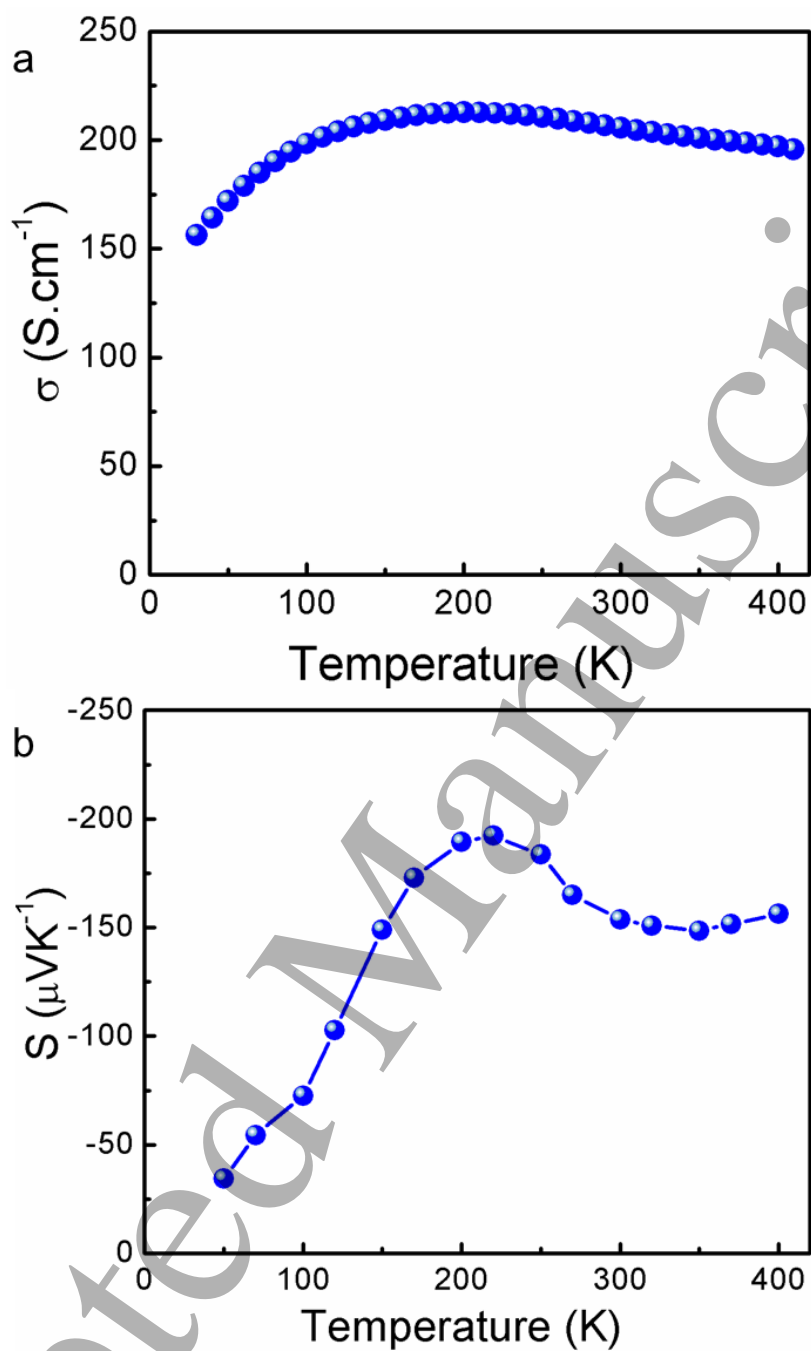


Fig. 5

